

***Amendments to the Claims***

1. (Canceled)

2. (Canceled)

3. (Canceled)

4. (Canceled)

5. (Canceled)

6. (Canceled)

7. (Canceled)

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13. (Canceled)

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18. (Canceled)

19. (Canceled)

20. (Canceled)

21. (Canceled)

22. (Original) A switched capacitor circuit, comprising:  
an integrator;

a summing junction switch connected to said integrator;  
a sampling capacitor connected to said summing junction switch;  
a signal conducting MOSFET switch connected to said sampling capacitor; and  
a replica network that controls a resistance of said signal conducting MOSFET switch, said replica network connected to a gate terminal of said signal conducting MOSFET switch.

23. (Original) The switched capacitor circuit of claim 22, wherein said integrator is a differential integrator and said summing junction switch is connected to an inverting terminal of said differential integrator.

24. (Original) The switched capacitor circuit of claim 23, further comprising:  
a second switch connected to a noninverting terminal of said differential integrator;  
a second sampling capacitor connected to said second summing junction switch;  
a second signal conducting MOSFET switch connected to said second sampling capacitor; and  
a second replica network that controls a resistance of said second signal conducting MOSFET switch, said second replica network connected to a gate terminal of said second signal conducting MOSFET switch.

25. (Canceled)

26. (Canceled)

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31. (Canceled)

32. (Canceled)

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34. (Canceled)

35. (Canceled)

36. (Canceled)

37. (Canceled)

38. (Canceled)

This listing of claims will replace all prior versions, and listings of claims in the application.